

August 1986 Revised March 2000

DM74LS367A Hex 3-STATE Buffer/Bus Driver

General Description

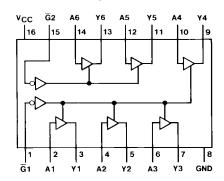
This device contains six independent gates each of which performs a non-inverting buffer function. The outputs have the 3-STATE feature. When enabled, the outputs exhibit the low impedance characteristics of a standard LS output with additional drive capability to permit the driving of bus lines without external resistors. When disabled, both the output transistors are turned OFF presenting a high-impedance state to the bus line. Thus the output will act neither as a significant load nor as a driver. To minimize the possibility that two outputs will attempt to take a common bus to opposite logic levels, the disable time is shorter than the enable time of the outputs.

Ordering Code:

Order Number	Package Number	Package Description
DM74LS367AM	M16A	16-Lead Small Outline Integrated Circuit (SOIC), JEDEC MS-012, 0.150 Narrow
DM74LS367AN	N16E	16-Lead Plastic Dual-In-Line Package (PDIP), JEDEC MS-001, 0.300 Wide

Devices also available in Tape and Reel. Specify by appending the suffix letter "X" to the ordering code.

Connection Diagram



Function Table

Inp	uts	Output					
Α	G	Y					
L	L	L					
Н	L	Н					
X	Н	Hi-Z					

H = HIGH Logic Level

L = LOW Logic Level X = Either LOW or HIGH Logic Level Hi-Z = 3-STATE (Outputs are disabled)

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Absolute Maximum Ratings(Note 1)

Supply Voltage 7V Input Voltage 7V Operating Free Air Temperature Range $0^{\circ}\text{C to } +70^{\circ}\text{C}$ Storage Temperature Range $-65^{\circ}\text{C to } +150^{\circ}\text{C}$

Note 1: The "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the Electrical Characteristics tables are not guaranteed at the absolute maximum ratings. The "Recommended Operating Conditions" table will define the conditions for actual device operation.

Recommended Operating Conditions

Symbol	Parameter	Min	Nom	Max	Units
V _{CC}	Supply Voltage	4.75	5	5.25	V
V _{IH}	HIGH Level Input Voltage	2			V
V _{IL}	LOW Level Input Voltage			0.8	V
I _{OH}	HIGH Level Output Current			-2.6	mA
I _{OL}	LOW Level Output Current			24	mA
T _A	Free Air Operating Temperature	0		70	°C

Electrical Characteristics

over recommended operating free air temperature range (unless otherwise noted)

Symbol	Parameter	Conditions		Min	Typ (Note 2)	Max	Units
VI	Input Clamp Voltage	$V_{CC} = Min, I_I = -18 \text{ mA}$				-1.5	V
V _{OH}	HIGH Level V _{CC} = Min, I _{OH} = Max		2.4	3.4		V	
	Output Voltage	$V_{IL} = Max, V_{IH} = Min$		2.7	5.4		·
V _{OL}	LOW Level	V _{CC} = Min, I _{OL} = Max	Min, I _{OL} = Max		0.05	0.5	
	Output Voltage	$V_{IL} = Max, V_{IH} = Min$			0.35	0.5	V
		I _{OL} = 12 mA, V _{CC} = Min			0.25	0.4	
I	Input Current @ Max Input Voltage	$V_{CC} = Max, V_I = 7V$				0.1	mA
I _{IH}	HIGH Level Input Current	$V_{CC} = Max, V_I = 2.7V$				20	μΑ
I _{IL}	LOW Level	$V_{CC} = Max$, $V_I = 0.5V$ (Note 5)	A Input			-20	μΑ
	Input Current	V _{CC} = Max, V _I = 0.4V (Note 6)	A Input			-0.4	mA
		$V_{CC} = Max, V_I = 0.4V$	G Input			-0.4	IIIA
lozh	Off-State Output Current with	$V_{CC} = Max, V_O = 2.4V$				20	μА
	HIGH Level Output Voltage Applied	$V_{IH} = Min, V_{IL} = Max$					
l _{OZL}	Off-State Output Current with	$V_{CC} = Max, V_O = 0.4V$				-20	
	LOW Level Output Voltage Applied	$V_{IH} = Min, V_{IL} = Max$				-20	μΑ
los	Short Circuit Output Current	V _{CC} = Max (Note 3)		-20		-100	mA
I _{CC}	Supply Current	V _{CC} = Max (Note 4)			14	24	mA

Note 2: All typicals are at $V_{CC} = 5V$, $T_A = 25$ °C.

Note 3: Not more than one output should be shorted at a time, and the duration should not exceed one second.

Note 4: $I_{\rm CC}$ is measured with the DATA inputs grounded and the OUTPUT CONTROLS at 4.5V.

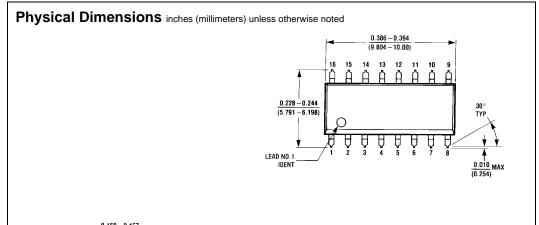
Note 5: Both $\overline{\mathsf{G}}$ inputs are at 2V.

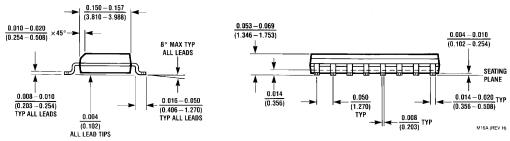
Note 6: Both \overline{G} inputs at 0.4V.

Switching Characteristics

at $V_{CC} = 5 V$ and $T_A = 25 ^{\circ} C$

	Parameter		$R_L = 667\Omega$				
Symbol		C _L =	C _L = 50 pF		C _L = 150 pF		
		Min	Max	Min	Max		
t _{PLH}	Propagation Delay Time LOW-to-HIGH Level Output		16		25	ns	
t _{PHL}	Propagation Delay Time HIGH-to-LOW Level Output		16		25	ns	
t _{PZH}	Output Enable Time to HIGH Level Output		30		40	ns	
t _{PZL}	Output Enable Time to LOW Level Output		30		40	ns	
t _{PHZ}	Output Disable Time from HIGH Level Output (Note 7)		20			ns	
t _{PLZ}	Output Disable Time from LOW Level Output (Note 7)		20			ns	
Note 7: C ₁ = 5 pF.							





16-Lead Small Outline Integrated Circuit (SOIC), JEDEC MS-012, 0.150 Narrow Package Number M16A

Physical Dimensions inches (millimeters) unless otherwise noted (Continued) 0.740 - 0.780 0.090 (18.80 - 19.81)(2.286)**16 15 14 13 12 11 10 9** 16 T5 F INDEX AREA 0.250 ± 0.010 (6.350 ± 0.254) PIN NO. 1 PIN NO. 1 1 2 3 4 5 6 7 8 1 2 _ OPTION 01 OPTION 02 0.065 $\frac{0.130 \pm 0.005}{(3.302 \pm 0.127)}$ (1.651) $\frac{0.060}{(1.524)}$ TYP 4° TYP 0.300 - 0.320 OPTIONAL (7.620 - 8.128) 0.145 - 0.200 $\overline{(3.683 - 5.080)}$ 95°±5° $\frac{0.008 - 0.016}{(0.203 - 0.406)} \text{ TYP}$ 90° ± 4° TYP 0.020 $\frac{0.280}{(7.112)}$ MIN (0.508)0.125 - 0.150 (3.175 - 3.810) 0.030 ± 0.015 (0.762 ± 0.381) 0.014 = 0.023 (0.356 = 0.584) 0.100 ± 0.010 (0.325 +0.040 -0.015 (2.540 ± 0.254) 0.050 ± 0.010 N16E (REV F) TYP (1.270 ± 0.254)

16-Lead Plastic Dual-In-Line Package (PDIP), JEDEC MS-001, 0.300 Wide Package Number N16E

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